

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	09989762
Filing Date	2001-11-19
First Named Inventor	Kenneth Y. Ogami
Art Unit	2123
Examiner Name	Osborne, Luke R.
Attorney Docket Number	CD01175M

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6411665	B1	2002-06-25	Chan et al.	Entire Document
	2	5867046	B1	1999-02-02	Sugasawa, Yasuo	Entire Document
	3	5517198	B1	1996-05-14	McEwan, Thomas E.	Entire Document
	4	5361290	B1	1994-11-01	Akiyama, Shin-ichiro	Entire Document
	5	6768352	B1	2004-07-27	Maher et al.	Entire Document
	6	6900663	B1	2005-05-31	Roper et al.	Entire Document
	7	3725804	B1	1973-04-01	Langan	Entire Document
	8	4833418	B1	1989-05-23	Quintus et al.	Entire Document

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9	5392784	B1	1995-02-28	Gudaitis	Entire Document
10	6292028	B1	2001-09-18	Tomita	Entire Document
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21	4809345	B1	1989-02-28	Tabata et al.	Entire Document
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24	6167077	B1	2000-12-26	Ducaroir	Entire Document
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1	20030135842	A1	2003-07-17	Frey et al.	Entire Document
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	1	USPTO Advisory Action for Application Number 11/200,619 (CD04112) dated 05/11/2009; 3 pages	<input type="checkbox"/>
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	4	Application Number 11/200,619 (CD04112): "Providing hardware independence to automate code generation of processing device firmware," Snyder et al.; 41 pages	<input type="checkbox"/>
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6	USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 10/21/2008; 12 pages	<input type="checkbox"/>
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17	Application Number 09/989,808 (CD01169M): "Automatic generation of application program interfaces, source code, interrupts, and data sheets for microcontroller programming," Bartz et al.; 67 pages	<input type="checkbox"/>
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28	USPTO Final Rejection for Application Number 09/989,761 (CD01179M) dated 08/26/2004; 7 pages	<input type="checkbox"/>
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31	USPTO Non-Final Rejection for Application Number 09/989,761 (CD01179M) dated 04/18/2003; 5 pages	<input type="checkbox"/>
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33	Wang, et al. "Synthesizing Operating System Based Device Drivers in Embedded Systems," 2003, ACM; 8 pages	<input type="checkbox"/>
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36	Xilinx, Virtex-II Pro Platform FPGA Developer's Kit, "How Data2BRAM Fits in with Hardware and Software Flows," Chapter 2: Using Data2BRAM; January 2003 Release; 2 pages	<input type="checkbox"/>
37	PCT Preliminary Report on Patentability (Chapter 1 of the Patent Cooperation Treaty), PCT/US2005/028793, filed 08/12/2005, mailed 12/21/2007; 2 pages	<input type="checkbox"/>
38	PCT Written Opinion of the International Searching Authority for PCT/US2005/028793, filed 8/12/2005, mailed 11/19/2007; 7 pages	<input type="checkbox"/>

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39	PCT International Search Report of the International Searching Authority for PCT/US05/28793, filed 08/12/2005, mailed 11/19/2007; 5 pages	<input type="checkbox"/>
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46	Application Number 12/132,527 (CD01175C): "System and Method for Performing Next Placements and Pruning of Disallowed Placements for Programming an Integrated Circuit." Ogami et al., 44 pages	<input type="checkbox"/>

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